

Fig. 1

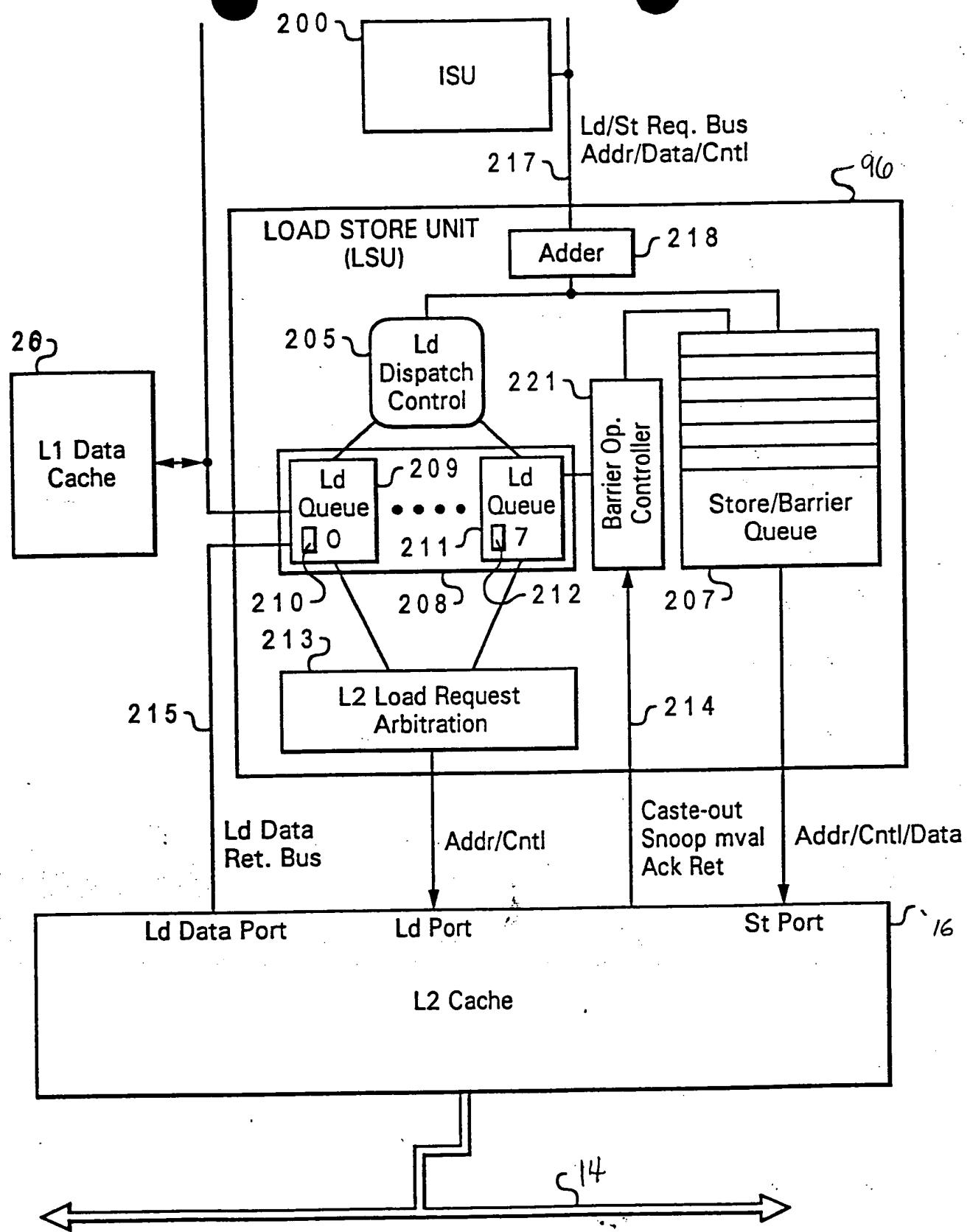


Fig. 2

FIG. 3

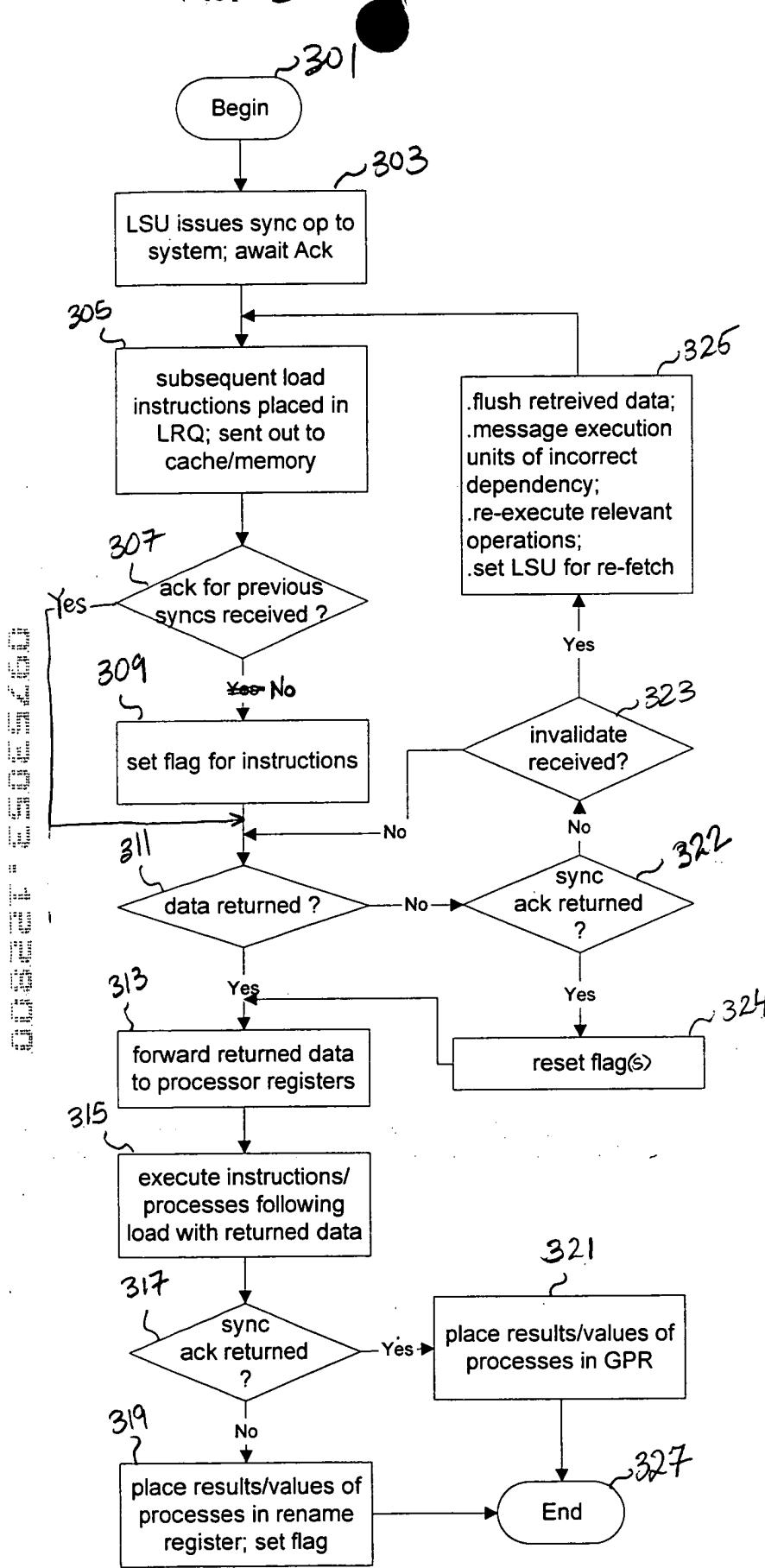


Figure 4

| No. | Flag | Register rename entries (values) | GPR # |
|-----|------|----------------------------------|-------|
| 1   | 0    |                                  | XXXX0 |
| 2   | 1    |                                  | XXX11 |
| 3   | 1    |                                  | XXX10 |
| .   |      |                                  |       |
| .   |      |                                  |       |
| .   |      |                                  |       |
| 80  | 0    |                                  | XX1XX |

FIGURE 5

500

(processor cycles) ----->>

